

INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Attorney Docket Number	3382-66774
		Application Number	Not Yet Assigned
		Filing Date	December 19, 2003
		First Named Inventor	Qadeer
		Art Unit	Unknown
		Examiner Name	Unknown
U.S. PATENT DOCUMENTS			
Examiner's Initials*	Cite No. (optional)	Number	Date
SP		6,546,443 B1	April 8, 2003
U.S. PATENT APPLICATION DOCUMENTS			
Examiner's Initials*	Cite No. (optional)	Publication Number	Publication Date
SP		US-2003/204570 A1	October 30, 2003
SP		US-2003/204641 A1	October 30, 2003
Examiner's Initials*	Cite No. (optional)	OTHER DOCUMENTS	
SP		Alur et al., "Modular Refinement of Hierarchic Machines," <i>In Proceedings of the 27th Annual ACM Symposium on Principles of Programming Languages</i> , pages 390-402, 2000.	
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SP		Ball et al., "Bebop: A Symbolic Model Checker for Boolean Programs," <i>In SPIN 00: SPIN Workshop, SPIN Model Checking and Software Verification</i> , Lecture Notes in Computer Science, Volume 1885, pages 113-130, Springer-Verlag, 2000.	
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EXAMINER SIGNATURE:	<i>Sam Al</i>	DATE CONSIDERED:	<i>12/22/03</i>
* Examiner: Initial if reference considered, whether or not in conformance with MPEP 609. Draw line through cite if not in conformance and not considered. Include copy of this form with next communication to applicant.			

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JP		Dwyer et al., "Data Flow Analysis for Verifying Properties of Concurrent Programs," <i>In Proceedings of the ACM SIGSOFT Symposium on foundations of Software Engineering</i> , pages 62-75, December 1994.	
JP		Esparza et al., "Efficient Algorithms for pre* and post* on Interprocedural Parallel Flow Graphs," <i>Annual Symposium on Principles of Programming Languages</i> , <i>Proceedings of the 27th ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages</i> , pages 1-11, 2000.	
JP		Esparza, "A BDD-based Model Checker for Recursive Programs," <i>In CAV'01, Proceedings of the 13th International Conference on Computer Aided Verification</i> , pages 324-336, 2001.	
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EXAMINER
SIGNATURE: *Sig Ahr*DATE
CONSIDERED: *2/2/07*

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